

WEST Search History

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DATE: Sunday, August 06, 2006

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<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>				
<input type="checkbox"/>	L43	voltage monitor and Intel.asn.		12
<input type="checkbox"/>	L42	L41 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		8
<input type="checkbox"/>	L41	L38 and L39		44
<input type="checkbox"/>	L40	L4 and L38		38
<input type="checkbox"/>	L39	(712/219).ccls.		313
<input type="checkbox"/>	L38	(712/217).ccls.		536
<input type="checkbox"/>	L37	register\$1 same L36		34
<input type="checkbox"/>	L36	(L33 or L34 or L35) same (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		63
<input type="checkbox"/>	L35	concatenat\$3 with value\$1		5235
<input type="checkbox"/>	L34	concatenat\$3 with result\$1		3092
<input type="checkbox"/>	L33	concatenat\$3 with data		8352
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>				
<input type="checkbox"/>	L32	L31 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		1
<input type="checkbox"/>	L31	concatenat\$3 with data		966
<input type="checkbox"/>	L30	(L28 or L29) and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		0
<input type="checkbox"/>	L29	concatenat\$3 with result\$1		124
<input type="checkbox"/>	L28	concatenat\$3 with value\$1		279
<input type="checkbox"/>	L27	L26 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		0
<input type="checkbox"/>	L26	merg\$3 with value\$1		595
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>				
<input type="checkbox"/>	L25	L24 same (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		4
<input type="checkbox"/>	L24	merg\$3 with value\$1		4987
<input type="checkbox"/>	L23	L2 same (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		1
<input type="checkbox"/>	L22	L2 with (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op or microoperation\$1 or micro-operation\$1)		1
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>				

<input type="checkbox"/>	L21	L20 with (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	14
<input type="checkbox"/>	L20	merg\$3 with register\$1	1955
<input type="checkbox"/>	L19	L18 not L5	2
<input type="checkbox"/>	L18	L17 same (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	14
<input type="checkbox"/>	L17	(eax or ax or al or ah) with (renam\$3 or re-nam\$3)	361
<input type="checkbox"/>	L16	L15 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	5
<input type="checkbox"/>	L15	(eax or ax or al or ah) with (reorder\$3 or re-order\$3)	248
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L14	(eax or ax or al or ah) and (reorder\$3 or re-order\$3)	15328
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L13	L12 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	0
<input type="checkbox"/>	L12	(eax or ax or al or ah) and (renam\$3 or re-Nam\$3)	61
<input type="checkbox"/>	L11	L10 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	0
<input type="checkbox"/>	L10	(eax or ax or al or ah) and (reorder or re-order)	80
<input type="checkbox"/>	L9	L8 and (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	0
<input type="checkbox"/>	L8	(eax or ax or al or ah) and retir\$5	125
<input type="checkbox"/>	L7	(eax or ax or al or ah) with retir\$5	2
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L6	L5 not L3	1
<input type="checkbox"/>	L5	L4 same (micro-instruction\$1 or microinstruction\$1 or uop or u-op or microop or micro-op)	13
<input type="checkbox"/>	L4	(eax or ax or al or ah) with retir\$5	169
<input type="checkbox"/>	L3	eax with retir\$5	23
<input type="checkbox"/>	L2	merg\$3 with value\$1 with register\$1	241
<input type="checkbox"/>	L1	6094719.pn.	1

END OF SEARCH HISTORY